

רשימת רכיבים של סדרת 74xxx הזמינים במלאי מחסן רכיבים

- 7400: Quad 2-input [NAND gate](#)
- 7401: Quad 2-input NAND gate with [open collector](#) outputs
- 7402: Quad 2-input [NOR Gate](#)
- 7403: Quad 2-input NAND Gate with open collector outputs (different pinout than 7401)
- 7404: Hex [Inverter](#)
- 7405: Hex Inverter with open collector outputs
- 7406: Hex Inverter Buffer/Driver with 30V open collector outputs
- 7408: Quad 2-input [AND gate](#)
- 7409: Quad 2-input AND gate with open collector outputs
- 7410: Triple 3-input NAND Gate
- 7411: Triple 3-input AND gate
- 7413: Dual [Schmitt trigger](#) 4-input NAND Gate
- 7414: Hex Schmitt trigger Inverter
- 7415: Triple 3-input AND gate with open collector outputs
- 7420: Dual 4-input NAND Gate
- 7421: Dual 4-input AND gate
- 7424: Quad 2-input NAND Gate gates with schmitt-trigger line-receiver inputs.
- 7425: Dual 4-input NOR Gate with Strobe
- 7427: Triple 3-input NOR Gate
- 7428: Quad 2-input NOR Buffer
- 7430: 8-input NAND Gate
- 7432: Quad 2-input [OR Gate](#)
- 7433: Quad 2-input NOR Buffer with open collector outputs
- 7436: Quad 2-input NOR Gate (different pinout than 7402)
- 7437: Quad 2-input NAND Buffer
- 7442: BCD to Decimal Decoder
- 7443: [Excess-3](#) to Decimal Decoder
- 7447: BCD to 7-segment Decoder/Driver with 15V open collector outputs
- 7454: 4-Wide 2-Input AND-OR-INVERT Gate
- 7470: AND-Gated Positive Edge Triggered J-K [Flip-Flop](#) with Preset and Clear
- 7473: Dual J-K Flip-Flop with Clear
- 7474: Dual D Positive Edge Triggered Flip-Flop with Preset and Clear
- 7475: 4-bit Bistable [Latch](#)
- 74LS78A: Dual Negative Edge Triggered J-K Flip-Flop with Preset, Common Clear, and Common Clock
- 7483: 4-bit Binary Full Adder
- 7485: 4-bit [Magnitude Comparator](#)
- 7486: Quad 2-input [XOR gate](#)
- 7490: Decade Counter (separate Divide-by-2 and Divide-by-5 sections)
- 7491: 8-bit [Shift Register](#), Serial In, Serial Out, Gated Input
- 7492: Divide-by-12 Counter (separate Divide-by-2 and Divide-by-6 sections)

- 7493: 4-bit Binary Counter (separate Divide-by-2 and Divide-by-8 sections)
- 74120: Dual Pulse Synchronizer/Drivers
- 74121: Monostable [Multivibrator](#)
- 74123: Dual Retriggerable Monostable Multivibrator with Clear
- 74125: Quad Bus Buffer with [Three-State](#) Outputs, Negative Enable
- 74126: Quad Bus Buffer with Three-State Outputs, Positive Enable
- 74132: Quad 2-input NAND Schmitt trigger
- 74136: Quad 2-Input [XOR gate](#) with open collector outputs
- 74138: 3 to 8-line Decoder/Demultiplexer
- 74139: Dual 2 to 4-line Decoder/Demultiplexer
- 74141: BCD to Decimal Decoder/Nixie Tube Driver
- 74145: BCD to Decimal Decoder/Driver
- 74147: 10-Line to 4-Line Priority Encoder
- 74148: 8-Line to 3-Line Priority Encoder
- 74151: 8-Line to 1-Line Data Selector/Multiplexer
- 74153: Dual 4-Line to 1-Line Data Selector/Multiplexer
- 74154: 4-Line to 16-Line Decoder/Demultiplexer
- 74155: Dual 2-Line to 4-Line Decoder/Demultiplexer
- 74157: Quad 2-Line to 1-Line Data Selector/Multiplexer, Noninverting
- 74160: Synchronous 4-bit Decade Counter with Asynchronous Clear
- 74161: Synchronous 4-bit Binary Counter with Asynchronous Clear
- 74163: Synchronous 4-bit Binary Counter with Synchronous Clear
- 74164: 8-bit Parallel-Out Serial Shift Register with Asynchronous Clear
- 74165: 8-bit Serial Shift Register, Parallel Load, Complementary Outputs
- 74166: Parallel-Load 8-Bit Shift Register
- 74174: Hex D Flip-Flop with Common Clear
- 74180 9-bit Odd/Even [Parity](#) Generator and Checker
- [74181](#): 4-bit Arithmetic Logic Unit and Function Generator
- 74192: Synchronous Up/Down Decade Counter with Clear
- 74193: Synchronous Up/Down Binary Counter with Clear
- 74194: 4-bit Bidirectional Universal Shift Register
- 74195: 4-bit Parallel-Access Shift Register
- 74240: Octal Buffer with Inverted three-state outputs
- 74242: Quad Bus Transceiver with Inverted three-state outputs
- 74243: Quad Bus Transceiver with Noninverted three-state outputs
- 74244: Octal Buffer with Noninverted three-state outputs
- 74245: Octal Bus Transceiver with Noninverted three-state outputs
- 74251: 8-line to 1-line Data Selector/Multiplexer with three-state outputs
- 74253: Dual 4-line to 1-line Data Selector/Multiplexer with three-state outputs
- 74257: Quad 2-line to 1-line Data Selector/Multiplexer with Noninverted three-state outputs
- 74258: Quad 2-line to 1-line Data Selector/[Multiplexer](#) with Inverted three-state outputs
- 74273: 8-bit Register with Reset
- 74292: Programmable Frequency Divider/Digital Timer
- 74298: Quad 2-Input Multiplexer with Storage
- 74299: 8-Bit Bidirectional Universal Shift/Storage Register with three-state outputs
- 74373: Octal Transparent Latch with three-state outputs

- 74374: Octal Register with three-state outputs
- 74377: 8-bit Register with Clock Enable
- 74541: Non-inverting Octal Buffer with three-state outputs
- 74573: Octal D-Type Transparent Latch with [Three-State](#) Outputs
- 74574: Octal D-Type Edge-Triggered Flip-flop with Three-state outputs
- 74590: 8-Bit Binary Counter with Output Registers and three-state outputs
- 74595: Serial-in Shift register with Output Registers **New**
- 74640: Octal Bus Transceiver with Inverting three-state outputs
- 74675 16bit Serial/Parallel shift reg (stock = 2)
- 74684: 8-bit Magnitude Comparator
- 74688: 8-bit Magnitude Comparator
- 74867